

**Amendments To The Claims**

1 - 26. (Canceled)

27. (Previously Presented) An implantable cardiac defibrillator device, comprising:

- a) an implantable pacemaker, wherein said pacemaker is configured to generate simultaneous anti-tachycardia pacing bursts such that said pacing bursts generate a blanking period; further comprising
- b) an atrial lead and a ventricular lead attached to said device, said atrial lead and said ventricular lead further comprising distal tip electrodes configured to deliver said simultaneous anti-tachycardia pacing bursts and detect an earliest arriving electrical signal and wherein said implantable cardiac defibrillator device is configured to determine [[an]] said earliest arriving electrical signal detected by said atrial lead distal tip electrodes or said ventricular lead distal tip electrodes following said blanking period resulting from said pacing bursts, wherein said earliest arriving electrical signal diagnoses an origin of an arrhythmia;
- b. an implantable cardiac defibrillator attached to said pacemaker; and
- c) a timing device connected to said pacemaker, said timing device configured to identify that said diagnosed origin of an arrhythmia is selected from between the group consisting of a supraventricular tachycardia, a ventricular tachycardia, and an atrioventricular nodal reentrant tachycardia, wherein said identification of said diagnosed origin of an arrhythmia is based upon said determination of said earliest arriving electrical signal.

28. (Previously Presented) The device of Claim 27, wherein said pacemaker further comprises a microprocessor configured to initiate said pacing burst.

29. (Previously Presented) The device of Claim 27, wherein said pacemaker generates said anti-tachycardia pacing burst.

30. (Canceled)

31. (Previously Presented) The device of Claim 27, wherein said atrial lead and said ventricular lead further comprise defibrillation electrodes.

32. (Canceled)

33. (Previously Presented) The device of Claim 27, wherein said pacemaker further comprises a storage memory connected to said atrial and ventricular leads.

34. (Previously Presented) The device of Claim 31, wherein at least one of said defibrillation electrodes is configured to convert an abnormal heart rhythm into normal sinus rhythm.

35. (Previously Presented) The device of Claim 27, wherein said atrial lead and said ventricular lead are quadripolar.

36. (Previously Presented) The method of claim 27, wherein said atrial lead and said ventricular lead further comprise separate conductors.

37. (Currently Amended) An implantable cardiac defibrillator device, comprising:

- a) an implantable pacemaker, wherein said pacemaker is configured to generate simultaneous anti-tachycardia pacing bursts such that said pacing bursts generate a blanking period; further comprising
- b) at least one atrial lead and at least one ventricular lead attached to said device, said at least one atrial lead and said at least one ventricular lead further comprising distal tip electrodes configured to deliver said simultaneous anti-tachycardia pacing bursts and wherein said implantable cardiac defibrillator device is configured to determine an earliest arriving electrical signal detected by said atrial lead distal tip electrodes or said

~~ventricular lead distal tip electrodes following said blanking period resulting from said pacing bursts, wherein said earliest arriving electrical signal diagnoses an origin of an arrhythmia;~~

- ~~b) an implantable cardiac defibrillator attached to said pacemaker; and~~
- ~~c) a timing device connected to said pacemaker, said timing device configured to identify that said diagnosed origin of an arrhythmia is selected from between the group consisting of a supraventricular tachycardia, a ventricular tachycardia and an atrioventricular nodal reentrant tachycardia, wherein said identification of said diagnosed origin of an arrhythmia is based upon said determination of said earliest arriving electrical signal.~~

38. (Previously Presented) The device of Claim 37, wherein said pacemaker further comprises a microprocessor configured to initiate said pacing burst.

39. (Previously Presented) The device of Claim 37, wherein said pacemaker generates said anti-tachycardia pacing burst.

40. (Previously Presented) The device of Claim 37, wherein said at least one atrial lead and said at least one ventricular lead further comprise defibrillation electrodes.

41. (Previously Presented) The device of Claim 37, wherein said pacemaker further comprises a storage memory connected to said atrial and ventricular leads.

42. (Canceled)

43. (Previously Presented) The device of Claim 37, wherein said at least one atrial lead and said at least one ventricular lead are quadripolar.

44. (Previously Presented) The method of claim 37, wherein said at least one atrial lead and said at least one ventricular lead further comprise separate conductors.